



Attorney's Docket No.: 10417-031001 / S21-119638M/HW

2875
#5/A

3-702
Mollie

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hisashi Shimizu et al.
Serial No. : 09/536898
Filed : March 28, 2000
Title : HYBRID INTEGRATED CIRCUIT DEVICE

Art Unit : 2875
Examiner : Wai-Sing Louie

Commissioner for Patents
Washington, D.C. 20231

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RESPONSE

In response to the action mailed September 13, 2001, please amend the application as follows:

In the claims:

Please cancel claim claims 2 and 3, which have been rewritten as claims 17 and 18 respectively.

Please amend claims 1, 4, 5, 8, and 14-16 as follows:

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-- 1. (Amended) A hybrid integrated circuit device comprising:

a hybrid integrated circuit substrate in which at least a surface is provided with insulation;

a first electrode and a second electrode formed on said surface;

a light emitting element connected with the first and second electrode;

a seal which is disposed in a periphery of said substrate; and

a transparent substrate which is fixed to said hybrid integrated circuit substrate via said seal to enclose the first and second electrodes and the light emitting element within a

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

December 13, 2001

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